

SERIAL MEDIA INDEPENDENT
INTERFACE WITH DOUBLE DATA
RATE
WILLIAM LO et al.
MP122/CUST. NO.: 23624
SHEET 1/6

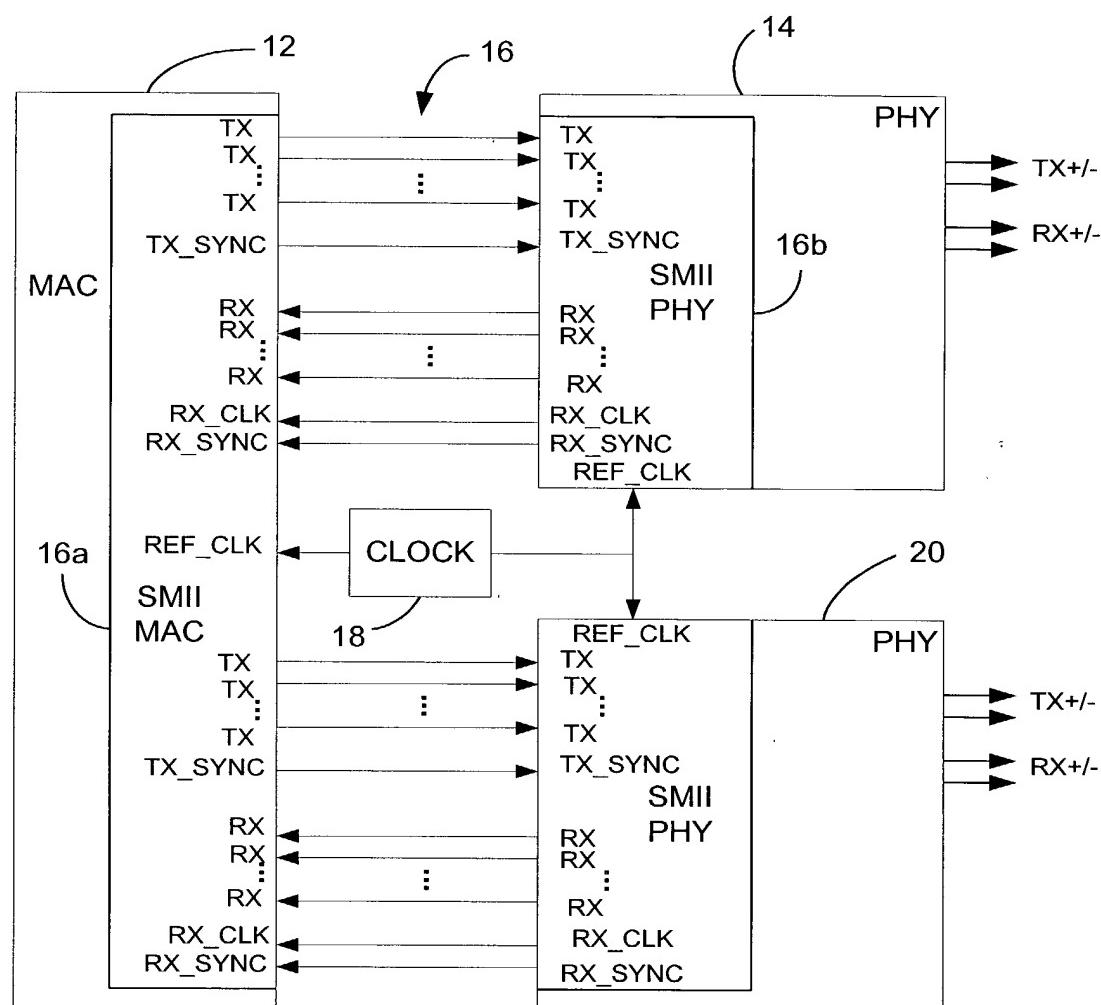
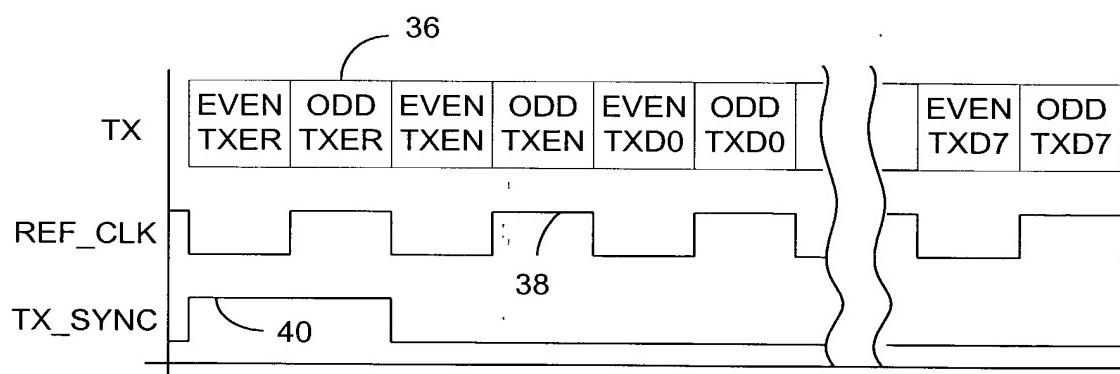
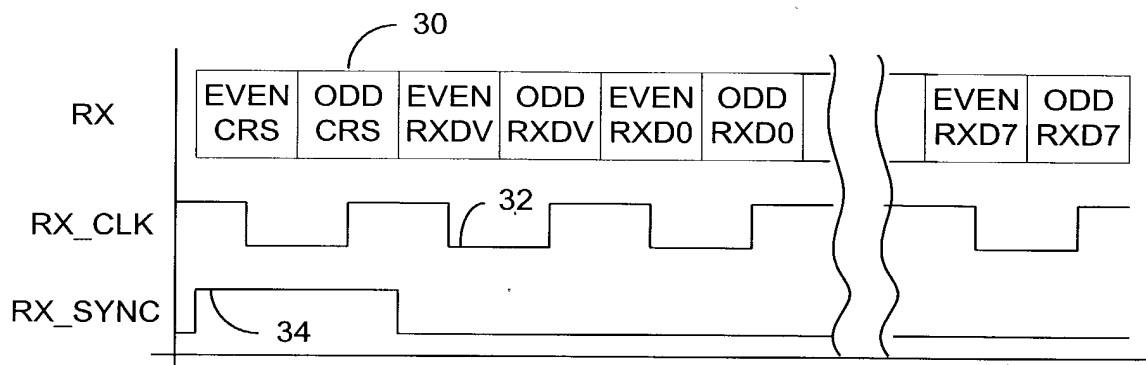
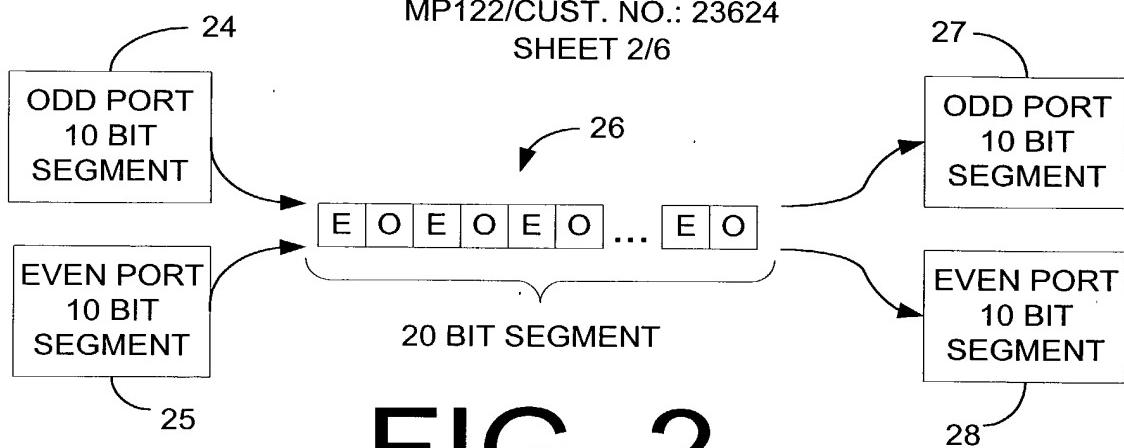


FIG. 1

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SERIAL MEDIA INDEPENDENT
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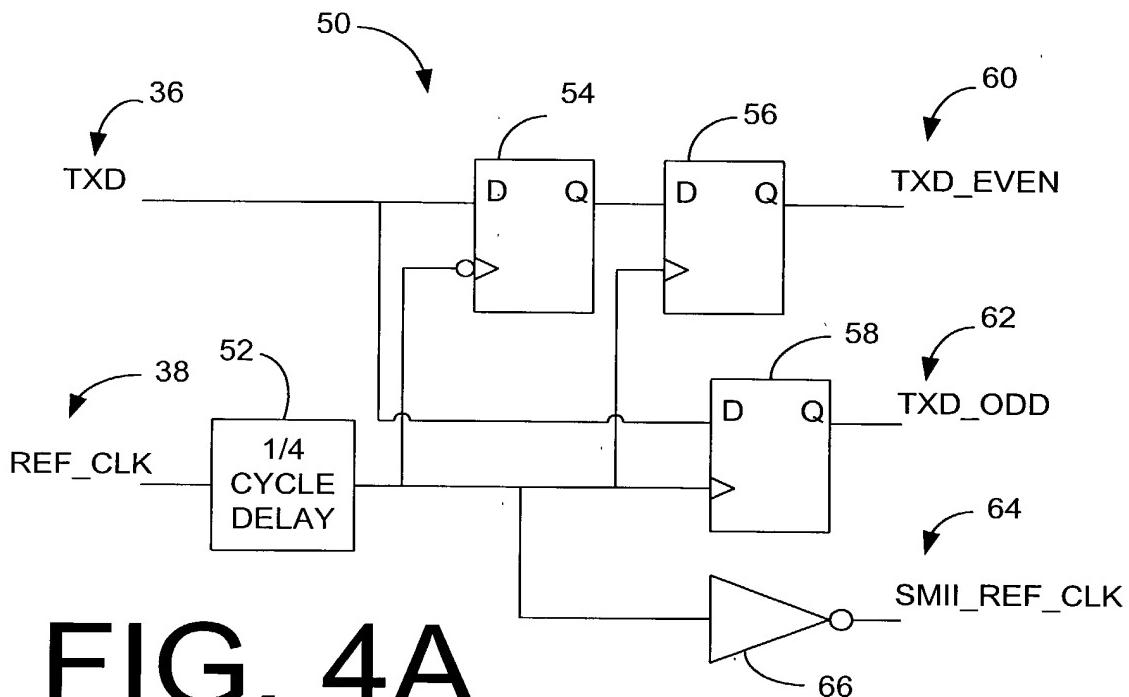


FIG. 4A

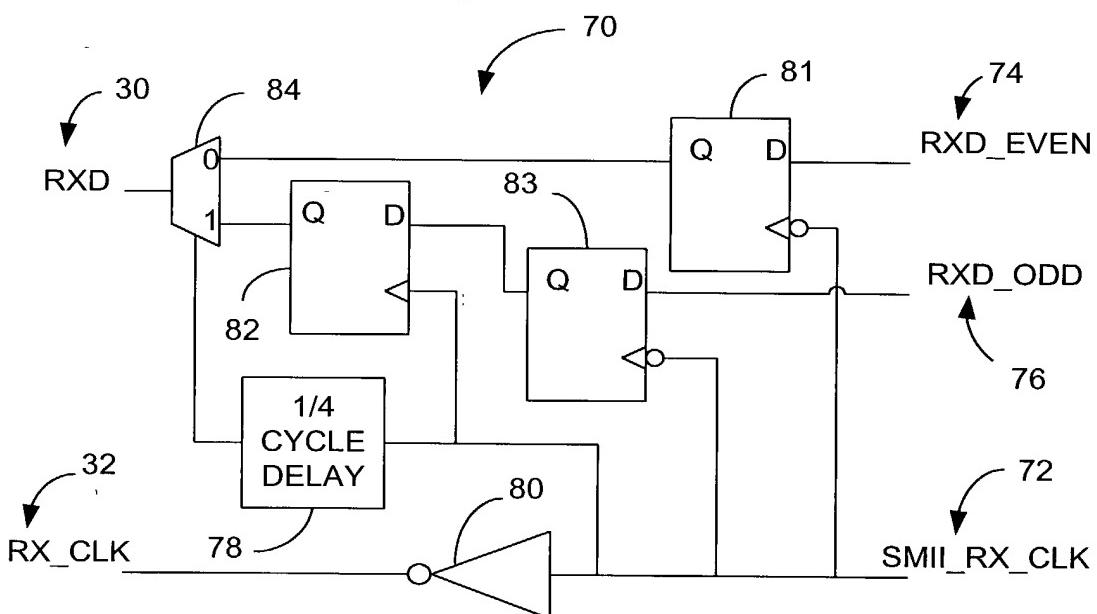


FIG. 4B

SERIAL MEDIA INDEPENDENT
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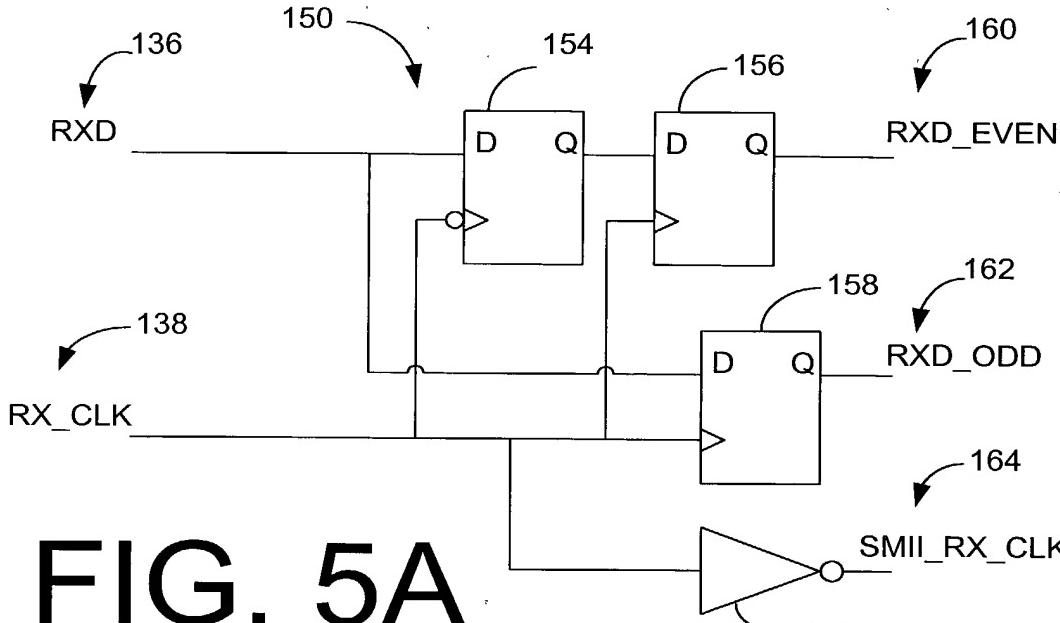


FIG. 5A

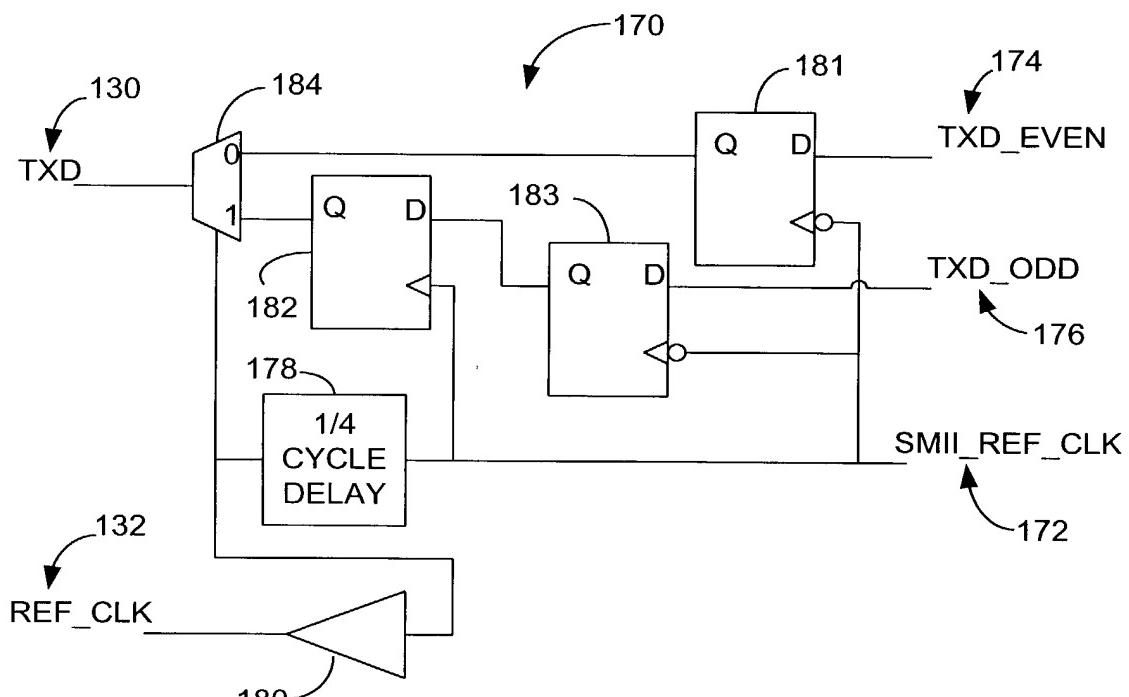


FIG. 5B

SERIAL MEDIA INDEPENDENT
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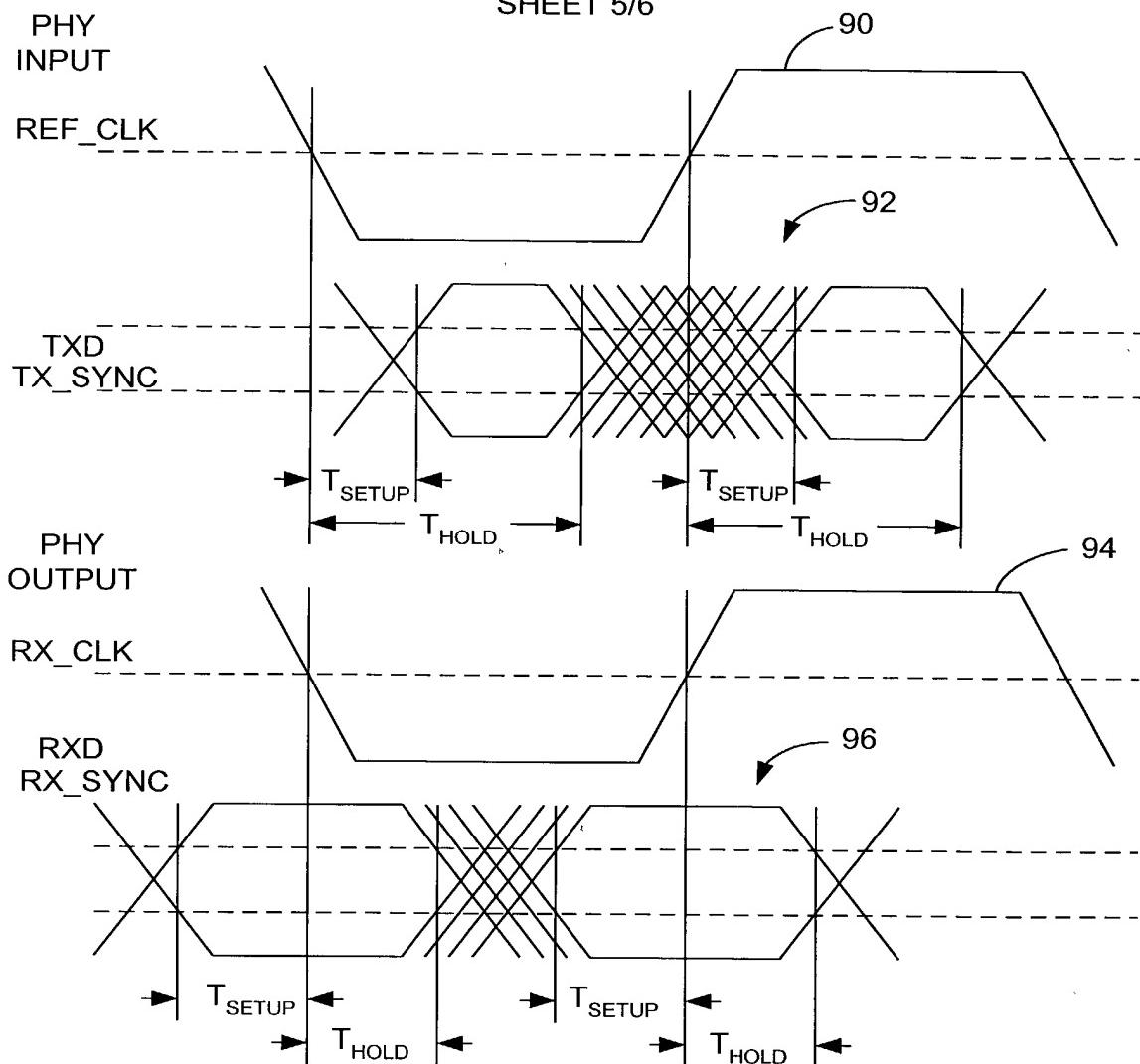


FIG. 6

SERIAL MEDIA INDEPENDENT INTERFACE WITH DOUBLE DATA RATE
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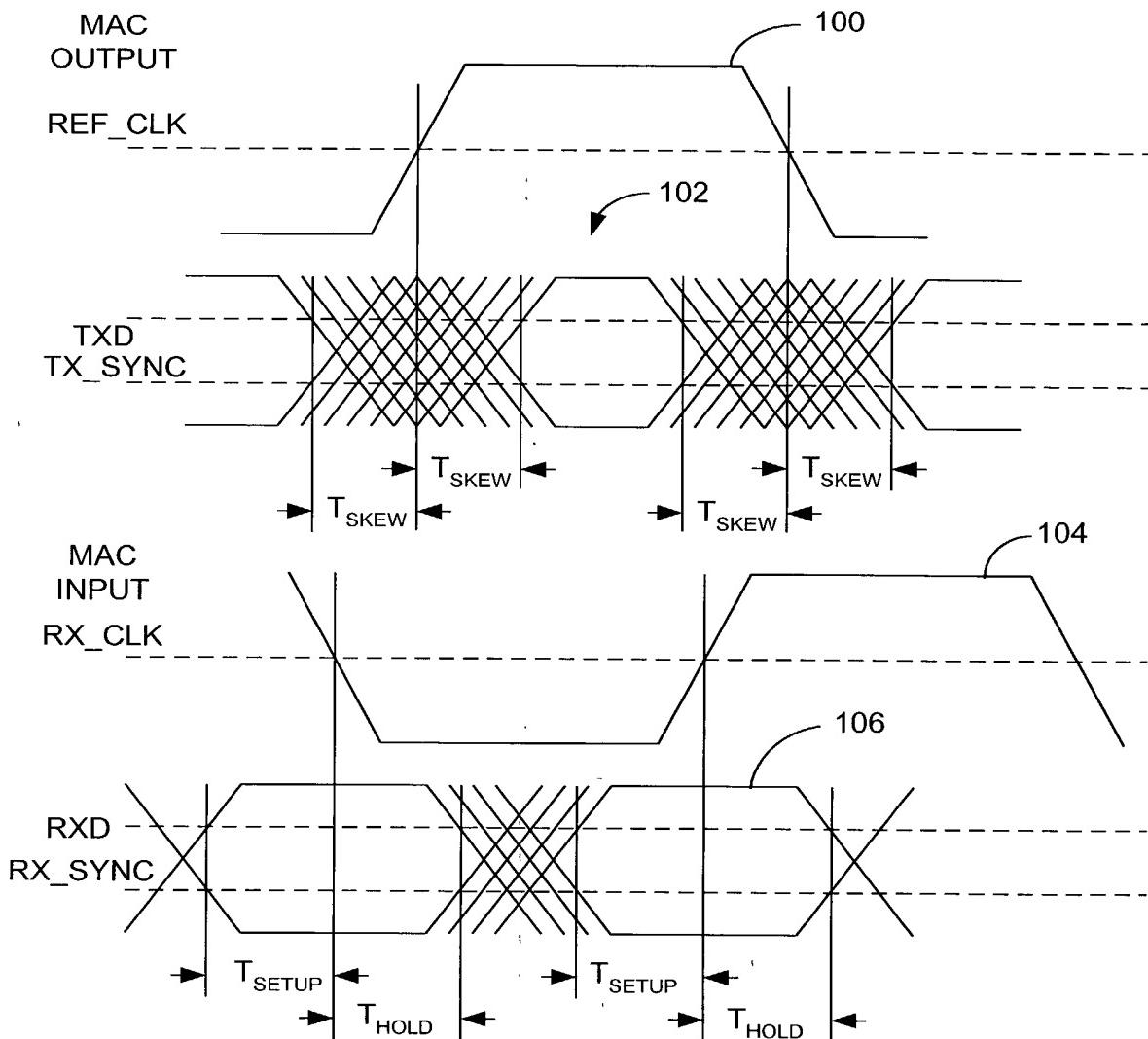


FIG. 7